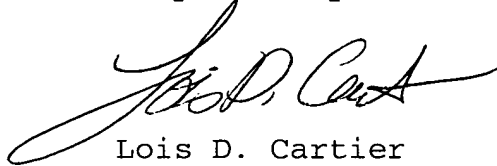


REMARKS

Attached hereto is a marked-up version of the changes made to the claims by the current amendment. The attached pages are captioned "Version with Markings to Show Changes Made."

No new matter has been introduced by any of the above amendments.

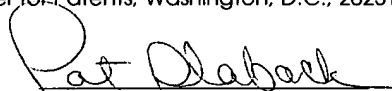
Respectfully submitted,



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I hereby certify that this correspondence is being deposited with the United States Postal Service as **first class mail** in an envelope addressed to: Commissioner for Patents, Washington, D.C., 20231, on November 12, 2002.

Pat Slaback
Name


Signature

VERSION WITH MARKINGS TO SHOW CHANGES MADE

CLAIMS

37. (New) A system, comprising:

a network; and

a transmitter coupled to the network and adapted to transmit a data frame to the network, the transmitter comprising a physical layer implemented in hard logic and a link layer implemented using programmable logic, the link layer being coupled between the physical layer and the network, the physical layer comprising:

a data node adapted to receive a data packet, the data packet including at least a portion of the data frame; and

a CRC generator adapted to perform a CRC on at least a portion of the data frame to provide a CRC value and to insert the CRC into the data packet.

38. (New) The system of Claim 37, wherein the link layer and the physical layer of the transmitter each comprise portions of a programmable logic device (PLD).

39. (New) The system of Claim 38, wherein the PLD is a field programmable gate array (FPGA).

40. (New) The system of Claim 37, further comprising:

a receiver coupled to the network and adapted to receive data from the network, the receiver being implemented in hard logic, the receiver comprising:

a physical layer having a data node adapted to receive a packet assembly from the network, the packet assembly including at least a portion of the data; and

a CRC generator adapted to perform a CRC on at least a portion of the packet assembly to provide a calculated CRC value.

41. (New) The system of Claim 40, wherein the link layer and the physical layer of the transmitter, and the physical layer and CRC generator of the receiver, each comprise portions of a programmable logic device (PLD).

42. (New) The system of Claim 41, wherein the PLD is a field programmable gate array (FPGA).

43. (New) A system, comprising:

a network; and

a receiver coupled to the network and adapted to receive data from the network, the receiver being implemented in hard logic, the receiver comprising:

a physical layer having a data node adapted to receive a packet assembly from the network, the packet assembly including at least a portion of the data; and

a CRC generator adapted to perform a CRC on at least a portion of the packet assembly to provide a calculated CRC value.

44. (New) The system of Claim 43, wherein the physical layer and CRC generator of the receiver each comprise portions of a programmable logic device (PLD).

45. (New) The system of Claim 44, wherein the PLD is a field programmable gate array (FPGA).